Re: Request for Correction

Our Ref.: B-5198NP 621388-9

Group: 2835

Examiner: unknown

) Date: June 2, 2005

of Inventor Information

UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(B): Debabani CHOUDHURY,

et al.

Serial No.: 10/716,196

Filing Date: November 17, 2003

For: "INTEGRATED CIRCUIT HOUSING"

MAIL STOP PGPUB Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The Applicants note that the publication of the above-identified application, U.S. Publication No. 2005/0082652 A1, contains an error in the address of the first-listed inventor. A copy of the front page of the publication is enclosed herewith for the Examiner's convenience.

The first-listed inventor's address and citizenship are listed on the published application as Thousand Oaks (IN). Please correct the inventor information as follows:

Debabani Choudhury, Thousand Oaks, CA (IN).

The Applicants respectfully request that the USPTO update their records accordingly.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first-class mail in an envelope addressed to the "Mail Stop PGPUB, Commissioner for Patents, P.O. Box/1450, Alexandria, VA

22313-1450" on June 2,/2,005 by Suzanne Johnston.

spectfully submitted,

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Enclosures: copy of first page of published application



(19) United States

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(54) INTEGRATED CIRCUIT HOUSING

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(57)**ABSTRACT**

A housing for an integrated circuit, comprising a base for securing a substrate with an integrated circuit thereon, a top cover, and a body with a cavity for receiving the substrate and at least a portion of the top cover therein to form an enclosed housing therewith, the body including at least one connector extending from within the cavity to outside of the body and configured to contact the integrated circuit when the substrate is in the cavity.

